

08-01.04

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT



In re application of:

Stephen Melvin

Mario D. Nemirovsky

Serial No.:

09/592106

Filed:

06/12/00

Docket:

MIPS.0167-00-US

For:

METHOD AND APPARATUS FOR IMPLEMENTING ATOMICITY OF

MEMORY OPERATIONS IN DYNAMIC MULTI-STREAMING

PROCESSORS

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 et seq. Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

Respectfully submitted.

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Date:

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Date of Deposit <u>8-4-04</u>. I hereby certify that this Date of Deposit 8-4-04. I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 C.F.R. §1.10 on the date shown above and is addressed to the U.S. Commissioner of

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary

Complete if Known	
Application Number	09/592106
Filing Date	06/12/00
First Named Inventor	Stephen Melvin
	Mario D. Nemirovsky
Group Art Unit	2154
Examiner Name	Larry D. Donaghue
Attorney Docket Number	MIPS.0167-00-US

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	AA	GRUNEWALD, WINFRIED et al., Towards Extremely Fast Context Switching in a Block-Multthreaded Processor, Proceedings of EUROMICRO-22, 1996, pp.592-599.
	АВ	BRADFORD, JEFFREY et al., Efficient Synchronization for Multithreaded Processors, Workshop on Multithreaded Execution, Architecture and Compilation, January-February 1998, pp.1-4
	AC	TULLSEN, DEAN et al., Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor, UCSD CSE Technical Report CS98-587, June 1998, all pages, US.
Examine Signatur		Date Considered

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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.